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GROUP 2500

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Eichelberger, C.

Serial No.: 08/650,628

Group Art Unit: 2508

Filed: 05/20/96

Examiner: Potter, R.

Title: SINGLE CHIP MODULES, REPAIRABLE MULTICHIP MODULES, AND
METHODS OF FABRICATION THEREOF

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Box Non-Fee Amendment, Washington, D.C. 20231, on October 02, 1997.

Kevin P. Radigan
Attorney for Applicant
Reg. No. 31,789

Date of Signature: October 02, 1997

To: Assistant Commissioner for Patents
Box Non-Fee Amendment
Washington, D.C. 20231

Response To Restriction Requirement

Dear Sir:

This paper is submitted in response to the September 3, 1997 Office Action issued in connection with the above-identified application. A response to said Office Action is due October 3, 1997, and therefore this paper is being timely filed.

In the Office Action, the Examiner required restriction to one of the following inventions under 35 U.S.C. §121:


I. Claims 1-24 and 56-59, drawn to a chip module, classified in class 257, subclass 723.

II. Claims 25-55, drawn to a method for fabricating or repairing a chip module, classified in class 437, subclass 180.

In response to this restriction requirement, applicant hereby elects claims 1-24 and 56-59 of Group I drawn to a chip module. This election to prosecute claims is submitted in compliance with 35 U.S.C. §121 and is made without prejudice to the method claims 25-55.

Consideration of the merits of claims 1-24 and 56-59 and a Notice of Allowance therefore are requested.

Respectfully submitted,



Kevin P. Radigan
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Reg. No. 31,789

Dated: October 02, 1997

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